

FIGURE 1

BEST AVAILABLE COPY

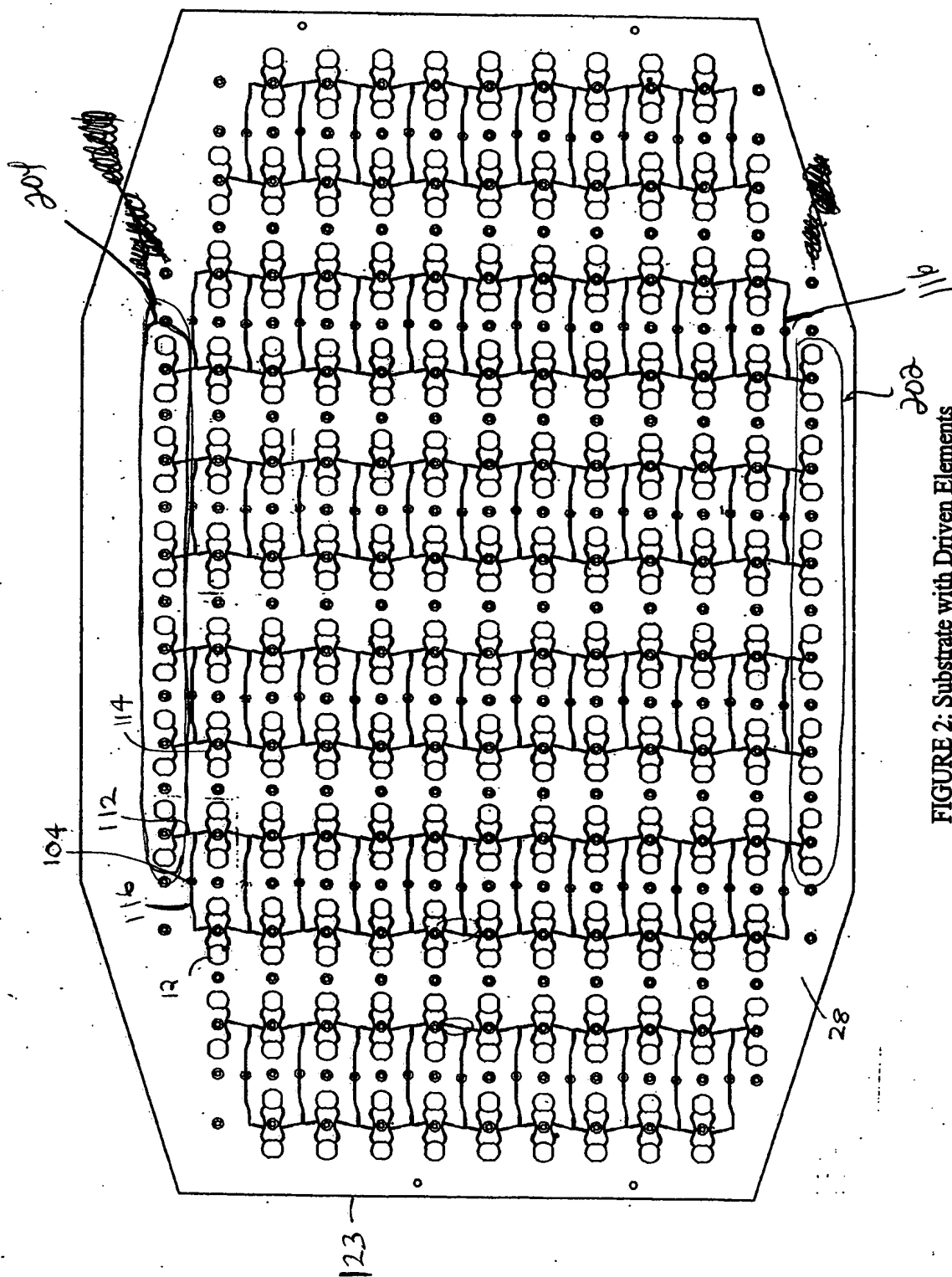


FIGURE 2: Substrate with Driven Elements

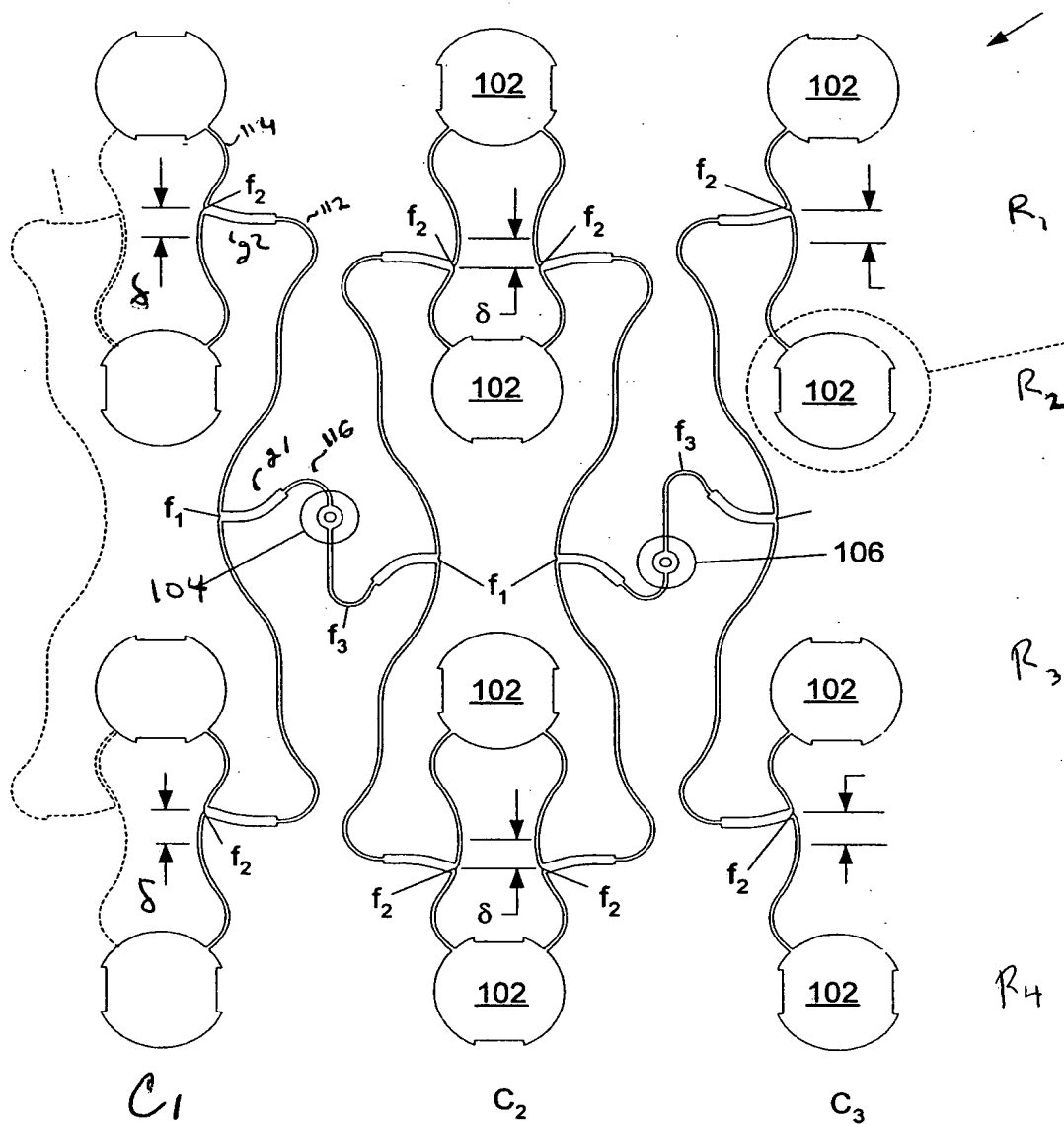


FIG. 3

BEST AVAILABLE COPY

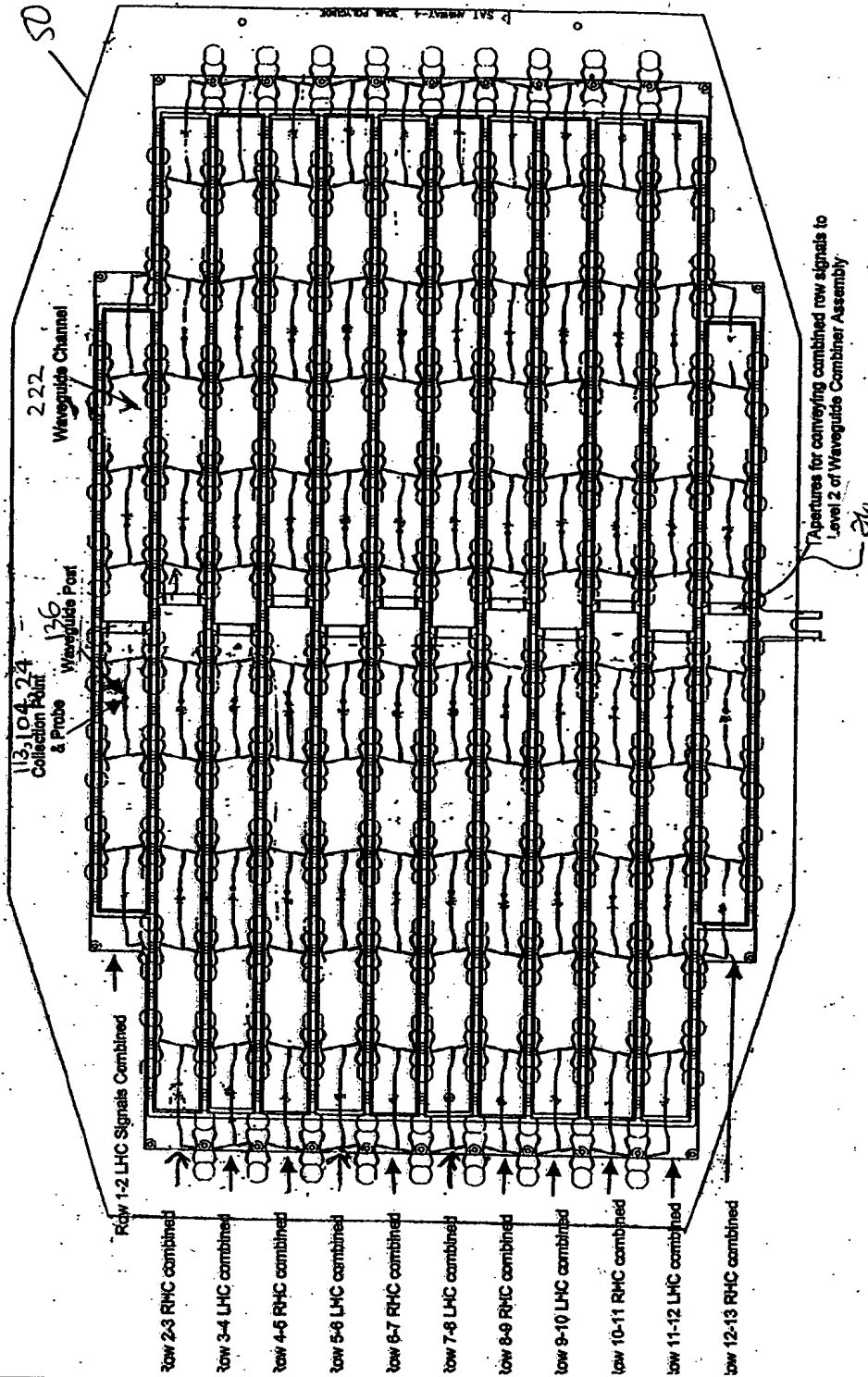
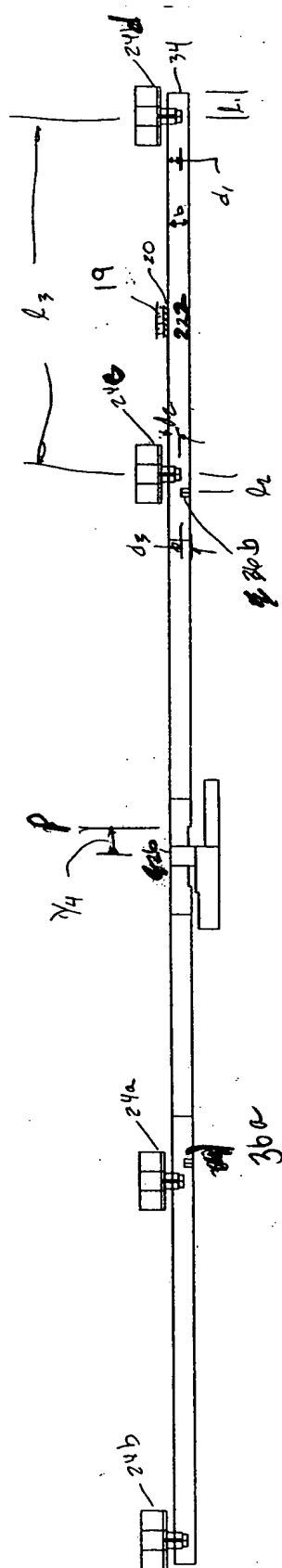
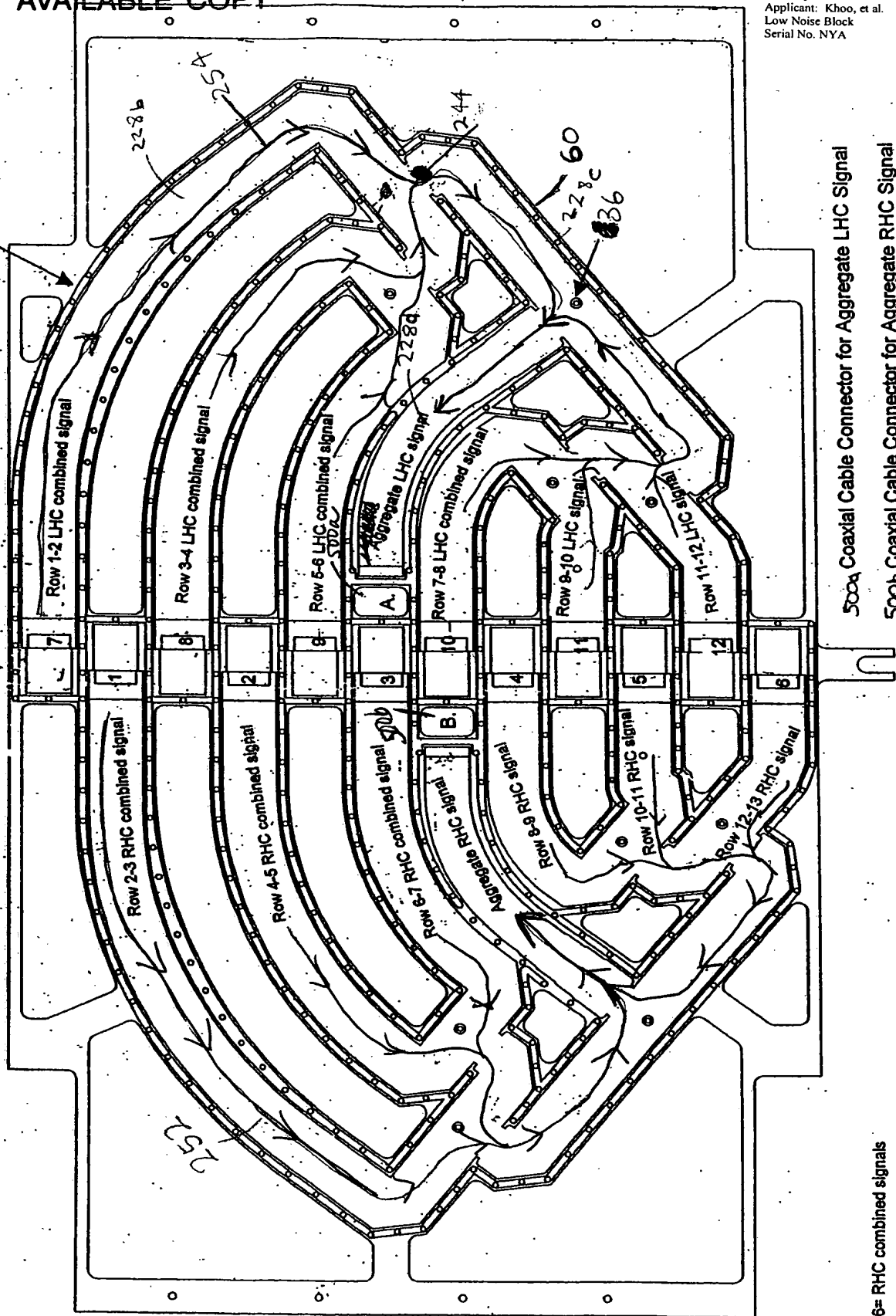


FIGURE 4: Level One of Waveguide Combiner Assembly (Showing Driven Patch Elements Above)



E-plane Junctions (1-12)

Arched Waveguide Walls



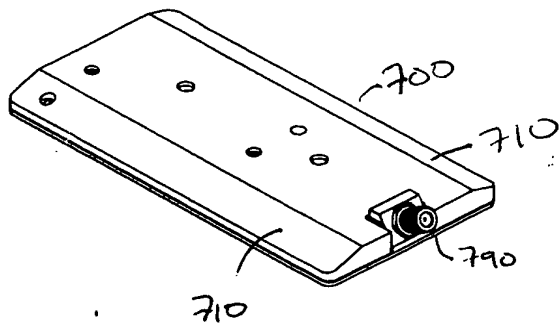


Figure 7B

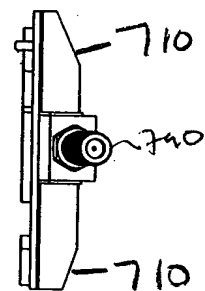


Figure 7C

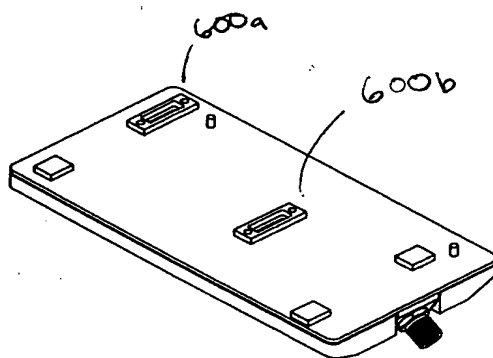


Figure 7A

FIGURE 7

# STACKED LNB

